## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE In respection In respection In the United States Patent and Trademark Office In respection In the United States Patent and Trademark Office In respection Stefanos Sidiropoulos, et al. Application No.: 09/372,879 Filed Date: Aug. 12, 1999 Title: Integrated Circuit Device Having I/O Structures With Reduced Input Loss Customer No.: 28554

## **POWER OF ATTORNEY**

Commissioner for Patents Alexandria VA 22313-1450

Sir:

I hereby appoint DeNIRO, Kirk J. (Reg. No. 35,854), HARMON, William J. (Reg. No. 40,635), MAGEN, Burt (Reg. No. 37,175), MARCUS, Brian I. (Reg. No. 34,511), and VIERRA, Larry E. (Reg. No. 33,809), as our attorneys to prosecute the application/patent identified above, and to transact all business in the United States Patent and Trademark Office connected therewith.

Please direct all telephone calls and change the correspondence address for the above-identified application/patent to:

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Rambus Inc. is the assignee of the entire right, title and interest in the above-identified patent application/patent. I, the undersigned, declare that I have reviewed copies of the documentary evidence establishing chain of title to the patent application/patent identified above from the inventors to the assignee which:

	is filed for recordation herewith; or
X	was recorded at Reel <u>010488</u> , Frame <u>0226</u> ; or
	has been sent for recordation under separate cover, a copy attached herewith.
The undersigned is authorized to sign this document on behalf of the assignee.	
Signature:_	SUBTRICE -
Date:	15/27/03
Name:	Kent Richardson
Title:	Vice President, Intellectual Property

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